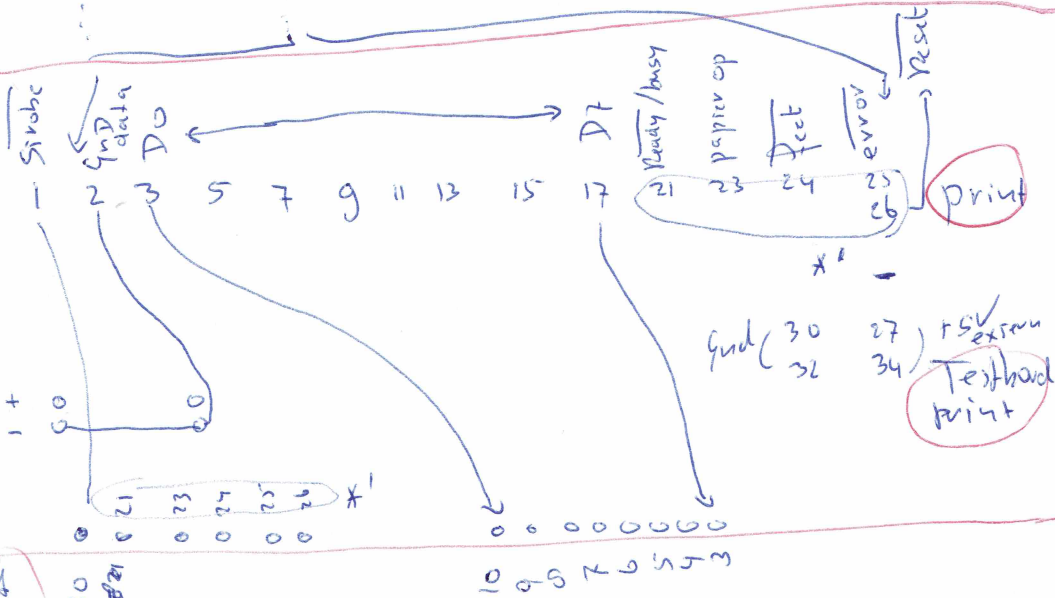




flat cable

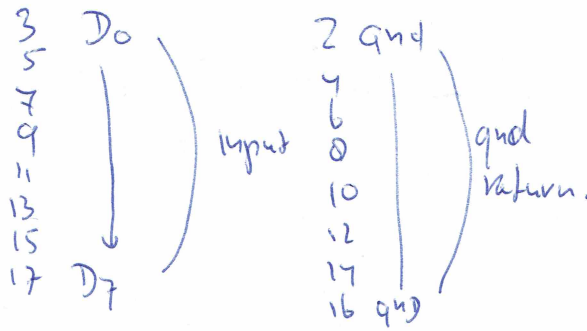


print

Testboard print

test board VIn

pin 1



+5V 27+34 power  
Gnd 32+30 3A

- 1 Strobe <sup>input</sup> ~~input~~
- 21 Ready/busy <sup>input</sup> ~~input~~ L=Ready output
- 23 paper out output
- 25 error output
- 24 Feed input
- 26 Reset input

Unit Data:  
① data output  
② Strobe   
③ Ready = low

1250

Richtung

pin

Auswahl

analog  
pin

OpM

1. strobe ← 13  $\sim \text{L} > 0,5 \mu\text{s}$

21 Ready / busy → 49

23 paper op → 50

25 error → 51

24 feed ← 52

26 reset ← 53

3 D0

5

7

9

11

13

15

17 D7

data ←

2A\*

4

1

5

2

6

4

7

8

9

16

10

32

11

64

12

128

# Schaltplan

